

AMENDMENTS TO THE SPECIFICATION

Please amend the specification, as follows:

Replace paragraph [0001] with the following amended paragraph [0001]:

This application claims priority [[of]] from Korean Patent Application No. 10-2003-0007156, filed February 5, 2003, in the Korean Intellectual Property Office (KIPO), the entire contents of which are hereby incorporated herein by reference.

Replace paragraph [0011] with the following amended paragraph [0011]:

The resampler 305 receives timing errors of symbols, which are obtained from baseband symbol processing, from the timing restoration unit 309 and performs interpolation to reduce an error between the digital signals. As a result of the interpolation, the digital signal sampled at a frequency of 24.69 MHz [[Mhz]] passes through the resampler 305, and then, the symbol rate of the digital signal is interpolated from n points of the original symbol rate

Replace paragraph [0024] with the following amended paragraph [0024]:

More specifically, the ADC 401 is capable of sampling an input signal and outputting the sampled signal to the PPF 402. Then, the PPF 402 may interpolate the received signal so as to change the data rate of this signal to a desired data rate. For instance, the ADC 401 samples the input signal at a frequency of 24.60 MHz [[Mhz]] and the PPF 402 converts the data rate of the signal output from the ADC 401 into a data rate at a frequency of 21.52 MHz [[Mhz]]. The signal having the desired data rate may be divided into the first signal with the real number component and the second signal with the imaginary number component. Here, the first signal is an in-phase (I) signal and the second signal is a ~~quadrature (Q)~~ quadrature-phase (Q) signal.

Replace paragraph [0034] with the following amended paragraph [0034]:

More specifically, the DC removal circuit 415 may include a first subtracter 417, a DC component restoration (DCR) circuit 421, and a second subtracter 419. The first subtracter 417 may subtract the second baseband signal QBB_i, whose frequency is shifted by the sort circuit 413, from the first baseband signal IBB_i, whose frequency is also shifted by the sort circuit 413.